

T0421 Sanlam NTA Junior Tournament
U/14 Girls

Main Draw

1	[1] Odycia KARAERUA				
2	A1 BYE	[1] Odycia KARAERUA			
3	Nieves BORGGREVE		[1] Odycia KARAERUA		
4	Frieda SHIPEPE	Nieves BORGGREVE Walkover	6-1, 6-2		
5	Hiturepi KAHUIKEE			[3] Ayanda BASSON	
6	Anelmie HERHOLDT	Hiturepi KAHUIKEE Walkover		6-1, 6-4	
7	BYE		[3] Ayanda BASSON		
8	A4 [3] Ayanda BASSON		6-1, 6-1		
9	[4] Mari VAN SCHALKWYK				[2] Joanivia BEZUIDENHOUT
10	A5 BYE	[4] Mari VAN SCHALKWYK			7-5, 6-3
11	Leandre LOUW		[4] Mari VAN SCHALKWYK		
12	Zikize MUVANGUA	Leandre LOUW 6-1, 6-3	6-0, 6-3		
13	Fransina UUGWANGA			[2] Joanivia BEZUIDENHOUT	
14	A7 BYE	Fransina UUGWANGA		7-5, 6-3	
15	BYE		[2] Joanivia BEZUIDENHOUT		
16	A8 [2] Joanivia BEZUIDENHOUT		6-0, 6-0		

Position 3-4

[1] Odycia KARAERUA	
[4] Mari VAN SCHALKWYK	[4] Mari VAN SCHALKWYK
	6-2, 6-2

Position 5-8

Nieves BORGGREVE	
Hiturepi KAHUIKEE	Nieves BORGGREVE
	7-6(8), 3-6, 10-6
Leandre LOUW	
Fransina UUGWANGA	Leandre LOUW
	6-3, 6-3
	Walkover

Position 7-8

Hiturepi KAHUIKEE	
D1	
BYE	Hiturepi KAHUIKEE

Event	State
E1	BYE
E2	BYE
E3	Zikize MUVANGUA
E4	BYE
E5	BYE
E6	Zikize MUVANGUA
E7	Zikize MUVANGUA
E8	Zikize MUVANGUA

BYE	
F1	
BYE	

The diagram illustrates a multi-processor system with a shared bus and a cache. The system components and their connections are as follows:

- Processor (G1):** Labeled "BYE". It is connected to the shared bus.
- Cache (G2):** Labeled "BYE". It is connected to the shared bus.
- Main Memory (G3):** Labeled "BYE". It is connected to the shared bus.

The shared bus is represented by a horizontal line. The processor and cache are connected to the bus via vertical lines. The main memory is connected to the bus via a vertical line. The bus is labeled "G3" in the center.

BYE	
H1	
BYE	